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### **Engineer, ic design & verification**

Jul 28, 2015 Engineer, IC Design & Verification City ENG-Hardware-IC Design Computer Science with interest in IC Design, IC Verification and simulation,

### **Design verification engineer - memory interface**

The selected candidate will be part of a team of Hardware design engineers involved in the design cycle of simulation, verification, debug and hardware

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Hardware Design Verification: Simulation and Formal Method-Based Approaches. By William K. Lam. ISBN-10: 0-13-701092-3 | ISBN-13: 978-0-13-701092-9 | 2005 Prentice Hall

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Current methods for designing embedded systems require to specify and design hardware and software from either formal verification or system co-simulation.

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FSM Based Functional Test Generation Framework for VHDL Lam, W.K.: Hardware Design Verification: Simulation and Formal Method-Based Approaches.

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Hi, I am using the Xilinx hardware compiler from CAL actors (Openforge). While simulating, I have problems of synchronization between verilog g

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